NOTES:

1. NO TIE PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS A1 AND 'C'.
2. CHAMFER EDGES MUST BE FREE OF CUTTING BURRS.

PCIE 164PIN,GEN5 SMT

Rc



DESCRIPTIO

-O 2022/08/31

TABLE

NO.	PART NO.	SPECIFICATION	HSF COMPLIANT
1		HOI	HF COMPLIANT

A

B

C

D

E

TOLERANCE X.X ±0.35 X.XX ±0.25 X.XXX ±0.10 ANGLE ±2.0°	MODEL NAME: UNIT:	DRAWN: CHECKED APPROVED:
	深圳市兴 - 冂 CNICS CO.,LTD.	DWG NO: